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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/537,986	03/29/2000	Neil Roy Hinson	2098/61624	6357

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Richard F Jaworski  
Cooper & Dunham LLP  
1185 Avenue of the Americas  
New York, NY 10036

EXAMINER
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NGUYEN, HUY THANH

ART UNIT	PAPER NUMBER
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2616

DATE MAILED: 06/02/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

**Application No.**

09/537,986

**Applicant(s)**

HINSON, NEIL ROY

**Examiner**

HUY T. NGUYEN

**Art Unit**

2616

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 09 May 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-13 and 15-40 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 2012 and 32-36 is/are allowed.
- 6) ☒ Claim(s) 1,13,15-31 and 37-40 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

## **DETAILED ACTION**

### ***Continued Examination Under 37 CFR 1.114***

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 09 May 2005 has been entered.

### ***Claim Rejections - 35 USC § 103***

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 1,13,15,16, 25,37- 40 rejected under 35 U.S.C. 103(a) as being unpatentable over Washino (5999220) in view of Hinson et al (5808628) .

Regarding claims 1 and 13, Washino teaches an editing system for storing a input moving image in a first format at a corresponding rate , producing an edited images for outputting at a second format at a corresponding rate ( column 10, lines 45-65), the editing system comprises :

a storage means for storing images of frames (414,412) (column 9, lines 60-68, column 10, lines 35-43)

an input circuit for receiving from a source data representing one or more sequences of image frames captured at a first frame rate, the data being received at an input data rate and representing each frame in a sequence as a first multiplicity of image lines which together form the image frame (column 10, lines 20-40)

an editing processor (416) for editing data read from the store at a processed data rate to produce data representing an edited sequence of image frames; and

an output circuit for outputting edited data representing an edited sequence of image frames at a second frame rate, the data being output at an output data rate and representing each frame in the edited sequence as a second multiplicity of image lines which together form the image frame (column 10, lines 25-33).

Washino fails to teach a random storage means for storing captured images of frames of a first frame format .

Hinson teaches editing apparatus utilizes a random storage means (11) for storing the input images of a first frame rate of a first format before editing the images ( Abstract column 8, lines 40-45.)

It would have been obvious to one of ordinary skill in the art to modify Washino with Hinson by using a random storage means as taught by Hinson with the apparatus of Washino for storing the input images of a first frame rate thereby enhancing the

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capacity of the apparatus of Washino in controlling and selecting the image of first frame rate to be edited to form the image of second format.

Regarding claim 15, Washino further teaches a sizing circuit for varying the number of lines between said first multiplicity and said second multiplicity in each frame as data is transferred to the output circuit (column 10, lines 25-33).

Regarding claims 16 and 25, Washino further teaches a monitor connected to receive data from the output circuit at said output data rate for display of said edited sequence thereon (Fig. 5).

Regarding claim 30, Washino further teaches a user operable input device; and wherein the editing processor is connected to the frame random access store and for processing data representing one or more image frames of one or more initial sequences in response to the user operable input device to produce processed data representing an processed, edited sequence, which processed data is stored in the frame-random access store (column 10, lines 25-68).

Regarding claims 31 and 37, Washino further teaches the one or more initial sequences are captured at a frame rate of 24 per second and each frame comprises 625 image lines, and the input circuit is arranged to receive the data and transfer the same to the store at an input rate substantially corresponding to 25 frames per second and 625 lines (column 9, lines 31-55, column 11) .

Regarding claim 38, Washino further teaches the processor is arranged to edit data at a processed data rate substantially corresponding to 24 frames per second and 625 lines, and the output circuit is arranged to output data at an output data rate

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substantially corresponding to the processed data rate (column 9, lines 31-55, column 11).

Regarding claim 39, Wasino further teaches the processor is arranged to edit data at a processed data rate substantially corresponding to 24 frames per second and 625 lines, and the output circuit is arranged to output data at an output data rate substantially corresponding to 30 frames per second and 525 line (column 9, lines 31-55, column 11).

Regarding claim 40, Washino further teaches the processor is arranged to extract frame data from the frame-random access store as pairs of interleaved fields and to repeat the transfer of one field of each pair to the output circuit thereby causing the output circuit to output the edited sequence at said frame rate of 30 per second (column 9, lines 31-55, column 11).

4. Claims 17, 18, 19, 20-23 and 27-29 are rejected under 35 U.S.C. 103(a) as being unpatentable over Wasino in view of Hinson as applied to claims 1,13,15,16, 25, further in view of Lang (4,963,995).

Regarding claims 17, 21 and 26, Washino fails to teach using videotape for storing the edited data. However, it is noted that using a video tape for storing the data from another apparatus is well known in the art as taught by Lang. Lang, at figure 2, column 9, discloses an editing apparatus comprises a VTR for receiving and storing the

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edited data. Therefore, it would have been obvious to one of ordinary skill in the art to modify Washino by using videotape for storing the editing data in order to preserve the edited data for later use.

Regarding claims 18, 22 and 27, Washino as modified with Lang further teaches that the video tape recorder selectively operable to output stored data at either said first frame rate or said second frame rate since Washino teaches means for changing the frame rate of stored data.

Regarding claims 19, 23 and 28, Washino further teaches a user operable input device; and wherein the editing processor is connected to the frame random access store and for processing data representing one or more image frames of one or more initial sequences in response to the user operable input device to produce processed data representing an processed, edited sequence, which processed data is stored in the frame-random access store (column 10, line 25 to column 11 line 68).

Regarding claims 20 and 29, Washino further teaches an editing system as claimed in claim 19, wherein said one or more initial sequences are captured at a frame rate of 24 per second and each frame comprises 625 image lines, and the input circuit is arranged to receive the data and transfer the same to the store at an input rate substantially corresponding to 25 frames per second and 625 lines (column 9, lines 31-55, column 11, lines 15-45).

***Allowable Subject Matter***

5. Claims 2-12 and 32-36 are allowed.

***Response to Arguments***

6. Applicant's arguments with respect to amended claims have been considered but are moot in view of the new ground(s) of rejection.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to HUY T. NGUYEN whose telephone number is (571) 272-7378. The examiner can normally be reached on 8:30AM -6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, James Groody can be reached on (571) 272-7950. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

H.N

  
HUY NGUYEN  
PRIMARY EXAMINER